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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10040868	FILING DATE 12/27/2001	CLASS 361/74	SUBCLASS 262	GAU 284	EXAMINER [Signature]
<b>**APPLICANTS:</b> Chang Tae-Sub; Lee Dong-Ho;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> REPUBLIC OF KOREA 2000-83571 12/28/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 9903-031	
Verified and Acknowledged Examiners's initials					
TITLE : High frequency semiconductor chip package and substrate					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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